



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patent
257/265
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TC 2800 MAIL ROOM

In re Application of:

Larry Leighton et al.

Serial No.: 09/813,604

Filed: March 20, 2001

Title: Bond Wire Tuning of Power Transistors
and Amplifiers

)
) Group Art Unit: 2826

)
) Examiner: Johannes P. Mondt

#11/C
11-8-02
Payton

AMENDMENT AFTER FINAL UNDER 37 CFR 1.116

5 Commissioner for Patents
Washington, D.C. 20231

Dear Sir:

10 In response to the Office Action mailed July 3, 2002, please amend the application as
follows:

In the Claims:

Sub E
15 1. (Amended) A method of manufacturing a power transistor circuit, comprising
carrying out the following steps in the enumerated order:

(1) securing a die to a substrate, the die comprising a transistor having an input
terminal;

(2) measuring a performance characteristic of the transistor;